

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Boon et al.

Serial No.: 10/717,421

Filed: November 19, 2003

For: CASTELLATED CHIP-SCALE PACKAGES AND METHODS FOR FABRICATING THE SAME

Confirmation No.: 7753

Examiner: Unknown

Group Art Unit: 2812

Attorney Docket No.: 2269-5660US

(02-1391.00/US)

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Po Service as First Class Mail on the date of deposit shown below with sufficient posta and in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

<u>April 2</u>8, 2004

Date

Deidra Pfeil
Name (Type/Print)

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 be considered by the Examiner and made of record. Copies of the listed documents are enclosed pursuant to 37 C.F.R. § 1.98(a). The listed documents were cited by the Office in co-pending application Serial No. 10/440,590, filed on May 19, 2003, and directed to a related invention.

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an

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admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicants herein that no other possible material information as defined in 37 C.F.R. § 1.56 (b) exists.

U.S. Patent Documents

U.S. Patent No.	Publication Date	<u>Patentee</u>	
US - 5,502,667	03/1996	Bertin et al.	

Applicants offer to supply any explanation or discussion of the document which the Examiner feels is necessary or desirable and which is requested.

This Supplemental Information Disclosure Statement is filed before the mailing date of a first Office Action on the merits.

Respectfully submitted,

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Date: April 28, 2004

BGP/dlm:djp

Enclosures: Form PTO-1449 or PTO/SB/08

Copy of documents cited

Document in ProLaw

PTO/SB/08A (10-01)

Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Substitute for	form 1449A/PTO			Complete if Known
INFORMATION DISCLOSURE			Application Number	10/717,421
STATEMENT BY APPLICANT		Filing Date	November 19, 2003	
		THE ELECTRICAL	First Named Inventor Boon et al.	
			Group Art Unit	2812
(use as many sheets as necessary)		Examiner Name	Unknown	
Sheet	1	o 1	Attorney Docket Number	2269-5660US (02-1391.00/US)

U.S. PATENT DOCUMENTS					
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	Cité No.	Number - Kind Code ² (if known)	MM-DD-YYYY	Document	Relevant Figures Appear
		US-5,502,667	03/1996	Bertin et al.	
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^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.